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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/507,357	09/10/2004	Patrik Jarl	P15400-US1	5702
27045	7590	03/18/2009	EXAMINER	
ERICSSON INC. 6300 LEGACY DRIVE M/S EVR 1-C-11 PLANO, TX 75024			GORTAYO, DANGELINO N	
			ART UNIT	PAPER NUMBER
			2168	
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			03/18/2009	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No. 10/507,357	Applicant(s) JARL, PATRIK	
	Examiner DANGELINO N. GORTAYO	Art Unit 2168	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 10 September 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 13-24 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 13-24 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 10 September 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date <u>9/10/2004</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Response to Amendment

1. In the amendment filed on 9/10/2004, Claims 1-12 have been cancelled. Claims 13-24 have been added. The currently pending claims considered below are Claims 13-24.

Information Disclosure Statement

2. An initialed and dated copy of Applicant's IDS form 1449, filed 9/10/2004, is attached to the instant Office action.

Specification

3. The abstract of the disclosure does not commence on a separate sheet in accordance with 37 CFR 1.52(b)(4). A new abstract of the disclosure is required and must be presented on a separate sheet, apart from any other text.

Claim Rejections - 35 USC § 112

4. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

5. Claim 16 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. Claim 16 recites the limitation "said channels" in line 1. There

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is insufficient antecedent basis for this limitation in the claim. Proper correction is required.

Claim Rejections - 35 USC § 102

6. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

7. Claims 13-24 are rejected under 35 U.S.C. 102(e) as being anticipated by Paneth et al. (US Patent 6,393,002 B1)

As per claim 13, Paneth teaches “A processing unit (PA) for processing a plurality of data streams by an algorithm divided into a plurality of process steps,” (see Abstract and column 1 lines 33-58)

“said PA comprising: an interconnection unit comprising means for switching;” (column 2 line 57 – column 3 line 4, Figure 2, column 8 lines 8-39, 57-67, column 9 lines 11-22, column 20 line 62 – column 25 line 13, wherein a channel control unit accepts streams from voice codec units and communicates with a remote-control processor unit in a base station to provide switching for a plurality of data streams)

“Process Step (PS) means comprising at least two PS modules, where each PS module is connected to the interconnection unit and a scheduler connected to said

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interconnection unit and to each PS module;” (Figure 5, column 25 lines 5-58, wherein modules are connected to a scheduler and the processing unit)

“a memory unit comprising at least two memories wherein each memory is connected to the interconnection unit;” (Figure 5, column 26 lines 7-28, wherein a database module contains memory connected to the modules)

“the interconnection unit further comprising means for providing at least a first connection between one of said memories and one of said PS modules and a second connection between another of said memories and another of said PS modules, wherein the interconnection unit is adapted to connect each memory to each of the PS modules by a switching activity, wherein the switching activity and the processing of the PS modules are controlled by the scheduler;” (column 8 lines 25-67, column 25 line 43 – column 26 line 28, wherein a remote-control processor unit is utilized to communicate between database module and scheduler for switching activity between a plurality of streams)

“and each memory comprises means for storing a data stream and said stored data streams are manipulated in parallel by the connected PS modules respectively, during a predetermined time period between said switching activities.” (column 7 lines 24-59, column 28 line 65 – column 29 line 60, wherein the database module is utilized to store and transmit data streams according to the scheduler that can utilize timer events)

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As per claim 14, Paneth teaches “at least one external memory for storing at least input and output data for the memories within the memory unit.” (column 35 lines 50-64)

As per claim 15, Paneth teaches “said data streams are channels in a communication system.” (column 1 line 63 – column 2 line 9, column 6 lines 20-35, column 7 lines 15-30)

As per claim 16, Paneth teaches “said channels are speech channels and said PA is implemented in a speech coder.” (column 7 lines 15-30, column 43 lines 10-46)

As per claim 17, Paneth teaches “said process step modules are implemented by means of hardware suitable for the algorithm.” (column 8 lines 57-67, column 24 line 62 – column 25 line 5)

As per claim 18, Paneth teaches “at least one of the PS modules transfer data between the external memory and any of the memories within the memory unit.” (column 35 lines 19-47)

As per claim 19, Paneth teaches “A method for processing a plurality of data streams by an algorithm divided into a plurality of Process Steps (PS) by using an interconnection unit comprising means for switching, Process Step (PS) means comprising at least two PS modules, each connected to the interconnection unit and a scheduler connected to said interconnection unit and to each PS module,” (see Abstract and column 1 lines 33-58)

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“said method comprising the steps of: connecting at least two memories within a memory unit to the interconnection unit;” (column 2 line 57 – column 3 line 4, Figure 2, column 8 lines 8-39, 57-67, column 9 lines 11-22, column 20 line 62 – column 25 line 13, wherein a channel control unit accepts streams from voice codec units and communicates with a remote-control processor unit in a base station to provide switching for a plurality of data streams)

“providing by the interconnection unit a first connection between one of said memories and one of said PS modules and a second connection between another of said memories and another of said PS modules, wherein the interconnection unit is adapted to connect each memory to each of the PS modules by a switching activity, wherein the switching activity and the processing of the PS modules are controlled by the scheduler;” (Figure 5, column 25 lines 5-58, wherein modules are connected to a scheduler and the processing unit)

“storing a data stream in each memory,” (Figure 5, column 26 lines 7-28, wherein a database module contains memory connected to the modules)

“and manipulating said data streams in parallel by the connected PS modules respectively, during a predetermined time period between said switching activities.” (column 7 lines 24-59, column 28 line 65 – column 29 line 60, wherein the database module is utilized to store and transmit data streams according to the scheduler that can utilize timer events)

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As per claim 20, Paneth teaches “storing at least input and output data for the memories within the memory unit at the at least one external memory.” (column 35 lines 50-64)

As per claim 21, Paneth teaches “said data streams are channels in a communication system.” (column 1 line 63 – column 2 line 9, column 6 lines 20-35, column 7 lines 15-30)

As per claim 22, Paneth teaches “said channels are speech channels and that said processing unit is implemented in a speech coder.” (column 7 lines 15-30, column 43 lines 10-46)

As per claim 23, Paneth teaches “said process step modules are implemented by means of hardware suitable for the algorithm.” (column 8 lines 57-67, column 24 line 62 – column 25 line 5)

As per claim 24, Paneth teaches “at least one of the PS modules transfers data between the external memory and any of the memories within the memory unit.” (column 35 lines 19-47)

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Yamamura (US Patent 6,981,134 B2)

Tsuboi (US Patent 6,414,993 B1)

North et al. (US Patent 6,055,619 A)

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to DANGELINO N. GORTAYO whose telephone number is (571)272-7204. The examiner can normally be reached on M-F 7:30-4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tim T. Vo can be reached on (571)272-3642. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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